

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

Application Number		10608846
Filing Date		2003-06-27
First Named Inventor	Glasco	
Art Unit	2186	
Examiner Name	Thai, T.	
Attorney Docket Number	NWISP030	

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6760809		2004-07-06	Animilli et al.	
	2	6148378		2000-11-14	Bordaz et al.	
	3	5829032		1998-10-27	Komuro et al.	
	4	6633945		2003-10-14	Fu et al.	
	5	5394555		1995-02-28	Hunter et al.	
	6	6018791		2000-01-25	Animilli et al.	
	7	6038652		2000-03-14	Van Huben et al.	
	8	6192451		2001-02-20	Animilli et al.	

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9	6330643		2001-12-11	Arinilli et al.	
10	6334172		2001-12-25	Arinilli et al.	
11	6405289		2002-06-11	Arinilli et al.	
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13	6631447		2003-10-07	Morioka et al.	
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20	6292705		2001-09-18	Wang et al.	
21	6615319		2003-09-02	Khare et al.	
22	5195089		1993-03-16	Sindhu	
23	6865595		2005-03-08	Glasco	
24	6640287		2003-10-28	Gharachorloo et al.	
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27	6067603		2000-05-23	Carpenter et al.	
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29	6704842		2004-03-09	Janakiraman et al.	
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31	5692123		1997-11-25	Logghe	
32	6467007		2002-10-15	Armstrong et al.	
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34	7003633		2006-02-21	Glasco	

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	2	20030009623		2003-01-09	Armilli et al.	
	3	20020052914		2002-05-02	Zalewski et al.	
	4	20020083243		2002-06-27	Van Huben	
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6	20040088493		2004-05-06	Glasco	
7	20030212741		2003-11-13	Glasco	
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	1	WO 0239242	WO		2002-05-16	SOKWOO et al.		<input type="checkbox"/>

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	3	KIM et al., "Power-aware Partitioned Cache Architectures", 2001 ACM p. 6467					<input type="checkbox"/>
	4	POWELL et al., "Reducing Set-Associative Cache Energy via Way-Prediction and Selective Direct-Mapping" 2001 IEEE, p. 54-65					<input type="checkbox"/>
	5	CULLER, D.E., J. P. Singh, A. Gupta, "Parallel Computer Architecture", 1999 Morgan Kaufmann, San Francisco, CA USA XP002277658					<input type="checkbox"/>
	6	TANENBAUM, Andrew, "Computer Networks", Computer Networks, London: Prentice Hall International, GB, 1996, pp. 345-403, XP002155220					<input type="checkbox"/>

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7	PCT Search Report PCT/US03/34756, Int'l filing date 10/30/03, Search report Mailed December 16, 2004 (NWISP024 WO)	<input type="checkbox"/>
8	BILIR et al., Multicast snooping: A New Coherence using a multicast address network, Computer Architecture, 1999, Proceedings of the 26th Int'l Symposium May 2-4, 1999	<input type="checkbox"/>
9	MARTIN et al., "Bandwidth Adaptive Snooping" High Performance Architecture, 2002, Proceedings. Eighth Int'l Symposium Feb. 2-6, 2002, pp. 251-262	<input type="checkbox"/>
10	SORIN et al., "Specifying and Verifying a Broadcast and a Multicast Snooping Cache Coherence Protocol" Parallel and Distributed Systems, IEEE Transactions, Vol. 13, Issue 6, June 2002	<input type="checkbox"/>
11	Application No: 10/288,347 (Now US Patent No. 7,003,633), First Office Action, dated November 18, 2004 (Atty Dkt: NWISP024)	<input type="checkbox"/>
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- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Godfrey Audrey Kwan/	Date (YYYY-MM-DD)	2007-06-11
Name/Print	Godrey Audrey Kwan	Registration Number	46850

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